

55 O.I.P.E. SCANNED <i>TC</i> Q.A. <i>MC</i>	PATENT DATE
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LOCATION NO. 69/519719	CONT/PRIOR	CLASS 438 156	SUBCLASS 345	PART UNIT 212 1763	EXAMINER ALEJANDRO
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APPLICANTS

Temperature controlled semiconductor processing chamber liner

FILE

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____		ISSUE FEE	
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